

- (b) What is Instruction Cycle? Explain Instruction Cycle with proper diagram. (7)
- Q-5 Attempt all questions (14)**
- (a) Explain ZERO, ONE, TWO, THREE address instruction in details. (7)
- (b) Differentiate between superscalar processor and vector processor. (7)
- Q-6 Attempt all questions (14)**
- (a) Write a short note on second pass of assembler. (7)
- (b) Draw architecture of General Register Organization. (7)
- Q-7 Attempt all questions (14)**
- (a) Differentiate between RISC and CISC. (7)
- (b) Write a short note on Accumulator unit. (7)
- Q-8 Attempt all questions (14)**
- (a) Explain Pipeline Processing using $P*Q+R$. (7)
- (b) Write a short note on bus and memory transfer. (7)

